

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	("20050229132").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/02 08:40
S2	757	phy near4 macro\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 11:45
S3	740	phy near2 macro\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 11:45
S4	48	clock and S2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 11:45
S5	36	((input or output) with buffer) and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 11:46
S6	29	((width or pitch or gap or spac\$3) with (path or channel)) and S5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 12:02
S7	2	(pci adj2 x) and S2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 12:04
S8	30	(pci adj2 x) and ((single with data with rate) or SDR)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 12:05
S9	16	((double with data with rate) or DDR) and S8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:56
S10	489	hard-macro\$4 or (hard adj macro\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 08:41
S11	524	hard-macro\$4 or (hard adj2 macro\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 13:39

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S12	579	hard-macro\$4 or (hard adj3 macro\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 13:39
S13	608	hard-macro\$4 or (hard adj4 macro\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:48
S14	35	(hard-macro\$4 or (hard adj4 macro\$4)) same (pitch or grid or spac\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:03
S15	23	((input or output or IO or I/O or input/output) with (buffer or cell or slot)) and S14	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:21
S16	0	(source adj synchronous) and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 13:47
S17	0	(source adj synchron\$7) and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 13:47
S18	0	(source near4 synchron\$7) and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 13:48
S19	5	synchron\$7 and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 13:51
S20	2	transceiver and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:09
S21	13	(hard-macro\$4 or (hard adj4 macro\$4)) same (pitch or grid or spacing)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:15
S22	3	clock and S21	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:04
S23	14	(hard-macro\$4 or (hard adj4 macro\$4)) same (pitch or grid or gap or spacing)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 16:32

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S24	164	(hard-macro\$4 or (hard adj4 macro\$4)) and (pitch or grid or gap or spacing)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:20
S25	70	clock and S24	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:20
S26	57	((input or output or IO or I/O or input/output) same (buffer or cell or slot)) and S25	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:51
S27	7	transceiver and S26	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:32
S28	9	(transmitter and receiver) and S26	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:49
S29	3941	transmitter and receiver and macro\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:49
S30	1689	((input or output or IO or I/O or input/output) same (buffer or cell or slot)) and S29	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 16:28
S31	356	((pitch or grid or gap or spacing or floor-plan\$4 or (floor adj plan\$4)) same (buffer or cell or slot)) and S30	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:54
S32	270	synchron\$7 and clock and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:54
S33	105	(source near4 synchron\$7) and S32	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 16:31
S34	32	((double with data with rate) or DDR) and S32	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 15:00
S35	1	((single with data with rate) or SDR) and S34	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:56

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S36	48	((single with data with rate) or SDR) and S32	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:58
S37	203	(data near2 bus) and S32	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:03
S38	89	(source near4 synchron\$7) and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 14:59
S39	34	((double with data with rate) or DDR or SDR or (single with rate with data)) and S38	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:10
S40	831	((double adj data adj rate) or DDR) and (SDR or (single adj rate adj data))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 10:41
S41	4	S30 and S40	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 15:12
S42	1727	((double with data with rate) or DDR) and (SDR or (single with data with rate))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 15:19
S43	18	S42 and S30	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 15:19
S44	713	(data near2 bus) and S30	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 16:28
S45	163	(source near4 synchron\$7) and S44	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 16:31
S46	162	bit and S45	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 16:31
S47	8	(hard-macro\$4 or (hard adj4 macro\$4) or hardmacro) and S46	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/01 16:33

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S48	1958	data same (pin near4 locat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 08:41
S49	114	(hard-macro\$4 or (hard adj macro\$4) or macro\$5) and S48	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 09:53
S50	94	(aling\$4 or adjust\$4 or arrang\$5) and S49	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:28
S51	47	((aling\$4 or adjust\$4 or arrang\$5) with (slot or pin or cell or buffer or input or output or IO or I/O or input/output or input-output)) and S49	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 08:46
S52	31	synchron\$7 and S51	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 08:46
S53	30	(data near4 bus) and S52	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 08:47
S54	30	clock and S53	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 09:53
S55	22	rout\$3 and S54	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 09:52
S56	22714	ball near3 grid near3 array	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 08:54
S57	1	S55 and S56	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 08:54
S58	4813	pad near3 pitch	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 10:42
S59	895	S56 and S58	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 09:52

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S60	24	(hard-macro\$4 or (hard adj macro\$4) or macro\$5) and S59	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:29
S61	3	clock and S60	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 09:53
S62	1	((double adj data adj rate) or DDR) and (SDR or (single adj rate adj data)) and S60	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 10:42
S63	3784	(pad or padding) same (pitch or grid or height or width) same (slot or zone)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:27
S64	37	(hard-macro\$5 or (hard adj macro\$5) or macro\$5) and S63	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 10:49
S65	3	(source near3 synchron\$7) and S64	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 10:52
S66	15	(layout or (lay\$2 adj out)) and S64	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 10:53
S67	3	synchron\$7 and S66	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:33
S68	2	(pitch with area) and S65	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 11:38
S69	2	((pitch or grid) with area) and S65	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 11:38
S70	17384	(pad or padding) same (pitch or grid or height or width) same (slot or zone or area)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:27
S71	12586	(aling\$4 or adjust\$4 or arrang\$5) and S70	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:28

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S72	304	(hard-macro\$5 or (hard adj macro\$5) or macro\$5) and S71	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:43
S73	1	((error near3 correct\$3 near3 code) or ECC) and S72	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:32
S74	49	((error near3 correct\$3 near3 code) or ECC) and S70	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:06
S75	1	(source near3 synchron\$7) and S74	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:45
S76	1	(source same synchron\$7) and S74	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:34
S77	4	(source and synchron\$7) and S74	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:34
S78	522	(hard-macro\$5 or (hard adj macro\$5) or macro\$5 or (intellectual adj property)) and S71	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:44
S79	704	(hard-macro\$5 or (hard adj macro\$5) or macro\$5 or (intellectual adj property) or IP) and S71	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 13:44
S80	4	(source near3 synchron\$7) and S79	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:25
S81	61	"6194252" "6523161" "6645841" "6083271" "6480995" "6510539" "6574780" "6584596" "6594811" "6625792"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:01
S82	20	((("6194252") or ("6523161") or ("6645841") or ("6083271") or ("6480995") or ("6510539") or ("6574780") or ("6584596") or ("6594811") or ("6625792"))).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/02 14:01
S83	2	"6743046"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:01

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S84	0	((error near3 correct\$3 near3 code) or ECC) and S81	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:02
S85	2	(source near3 synchron\$7) and S81	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:07
S86	8	(data near4 bus) and S81	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:04
S87	3	clock and S86	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:04
S88	25549	error adj correct\$3 adj code	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:07
S89	223	(source near2 synchron\$7) and S88	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:07
S90	17	layout and (pad or padding) and S89	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:08
S91	17	clock and S90	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:08
S92	17	(transceiver or transmit\$3 or receiv\$3) and S91	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:09
S93	1	((double with data with rate) or DDR) and (SDR or (single with rate with data)) and S92	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:11
S94	16	((double with data with rate) or DDR or SDR or (single with rate with data)) and S92	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 14:11
S95	3	(bus with bit) and S80	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:56

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S96	18	"6426649" "6483343" "6608500" "6674303"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:56
S97	3	layout and S96	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:57